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## APPLICANTS

Tetsuo Sasaki, Hadano, JAPAN;

Yousuke Nagao, Hadano, JAPAN;  
Tatsuki Ishii, Hinode, JAPAN;Itaru Matsumoto, Ebina, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

*wk*      *myf*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-038678 02/15/2001

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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 7	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 5
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Examiner's Signature</i> <i>Initials</i>				

## ADDRESS

Mattingly, Stranger & Malur, P.C.  
Suite 370  
1800 Diagonal Road  
Alexandria , VA  
22314

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## TITLE

Design method and system for achieving a minimum machine cycle for semiconductor integrated circuits

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